

Brennan 2-81-1-4

CONFIRMATION NO. 7750  
DATE OF NOTICE OF ALLOWANCE: March 7, 2007  
SERIAL NO. 10/773,614

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Patent Application**

Applicant(s): J.M. Brennan et al.

Case: 2-81-1-4

Serial No.: 10/773,614

Filing Date: February 6, 2004

Group: 2823

Examiner: Brook Kebede

Title: Semiconductor Device Configured for  
Reducing Post-Fabrication Damage

---

**AMENDMENT AFTER ALLOWANCE UNDER 37 C.F.R. §1.312**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Claims 2-7 and 9-25 of the above-identified application were allowed on March 7, 2007.  
Applicants respectfully request amendment of the application as follows prior to issuance.